

Registers

Location in memory

GPR - General purpose register → data via databus
SFR - Special function register → data via databus and
direct on silicon.

How do you move data along the data bus?

60 sample moving average.

Once per second ADC result is stored in
ADRESH.

ADRESH

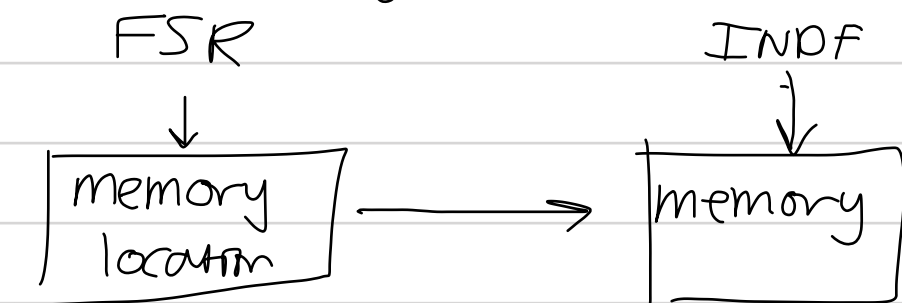
↓ movf ADRESH, W

WREG

↓ movwf 0x01

move mod(counter, 64) to FSR
movwf INDF.

Indirect addressing:



EEPROM

Electrically Erasable Programmable Read only memory

NB: Limited read-write cycles.