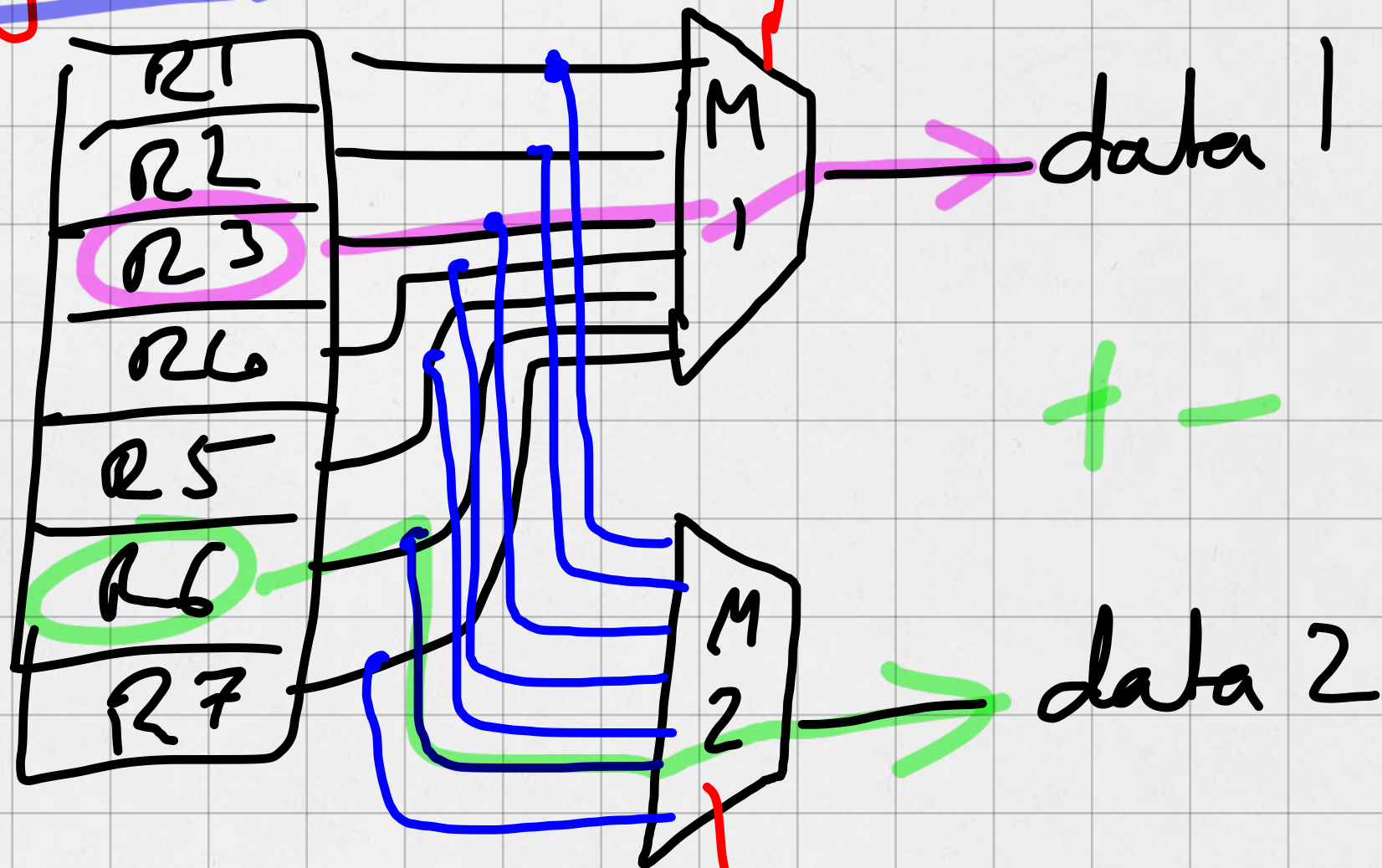


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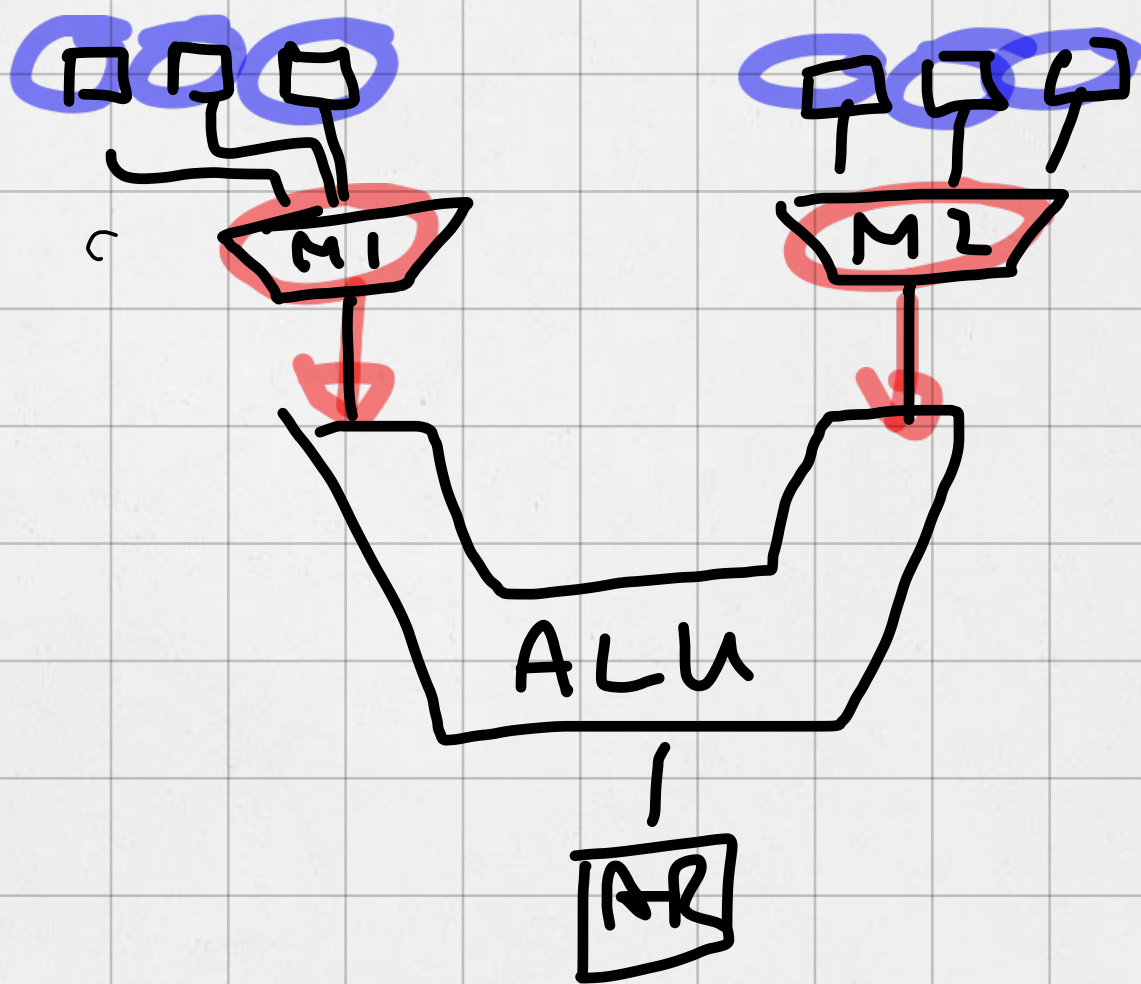
Arithmetic Logic unit (ALU)

READ/WRITE to registers

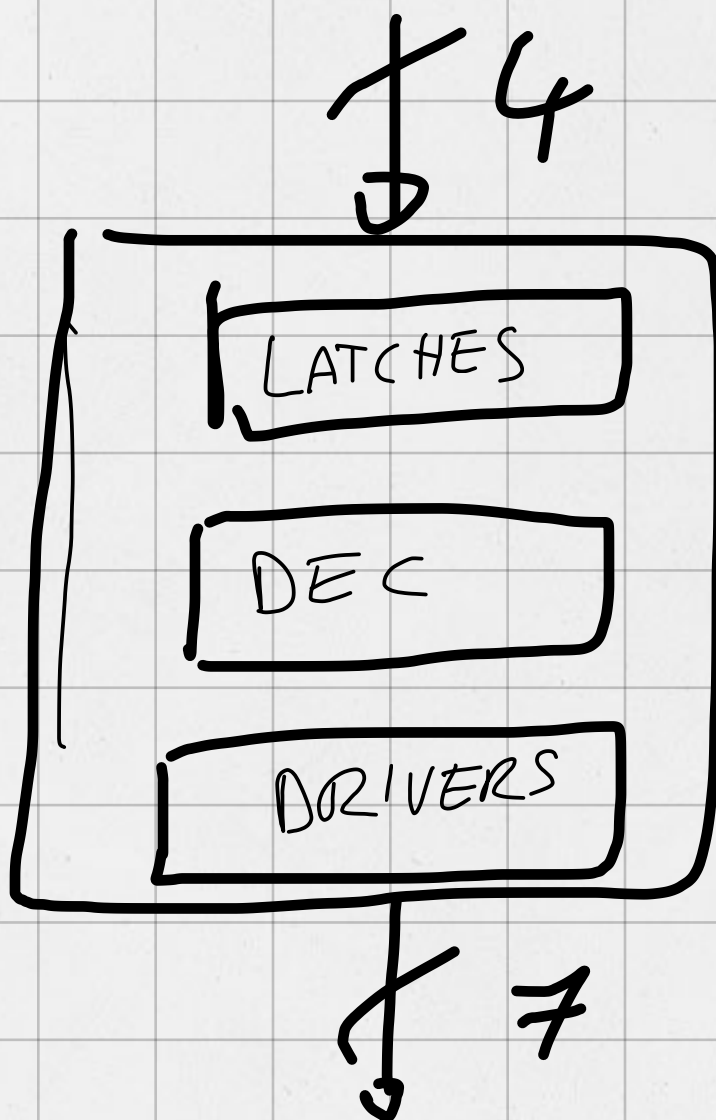
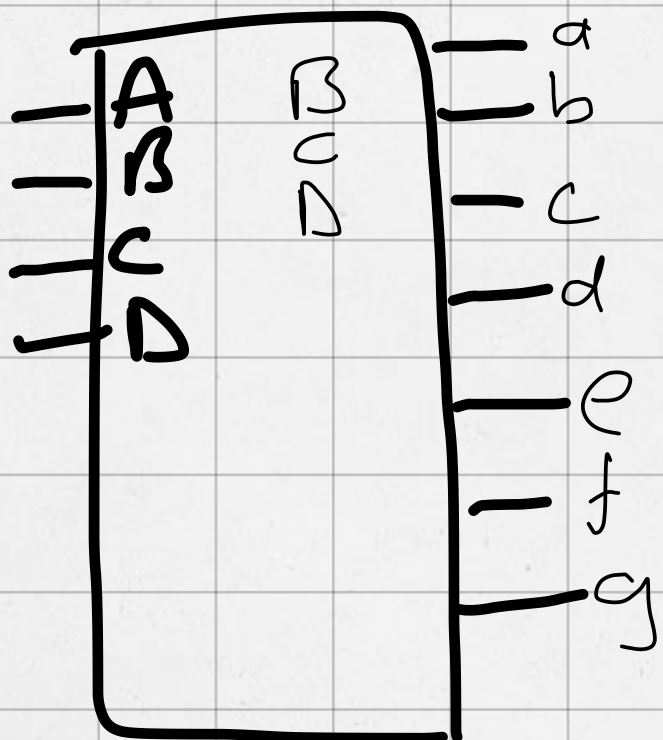
Reg select



Reg select









At the end of the lecture the projector stopped working →

→ Drew on the board an "up" "down" counter

→ Went into a discussion on computational paradigms

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